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Attorney's Docket No.: 10559-391001/P10256 - ADI APD1812-1-US/Intel Corporation

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

JUL 0 1 2004

Applicant : Ryo Inoue et al.

Art Unit: 2183 .

Examiner: Charles A.

OFFICIAL Harkness

Serial No.: 09/745,104

Assignee : Intel Corporation

: December 20, 2000

Filed Title

: HARDWARE LOOPS AND A PIPELINE SYSTEM USING ADVANCED

GENERATION OF LOOP PARAMETERS (AS AMENDED)

Mail Stop Final

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

AMENDMENT

In response to the Office action mailed April 2, 2004, please amend the application as follows:

Amendments to the Claims begin on page 2 of this paper.

Remarks/Arguments begin on page 12 of this paper.

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CERTIFICATE OF TRANSMISSION BY FACSIMILE

I hereby excrisfy that this correspondence is being transmitted by facaimile to the Potent and Trademark Office on the date indicated

PAGE 2/15 * RCVD AT 7/1/2804 7:46:24 PM [Eastern Daylight Time] * SVR:USPTO-EFXRF-1/8 * DNIS:8729306 * CSID:8586785099 * DURATION (mm-ss):09-20